

LVDS SURFACE MOUNT VCXO

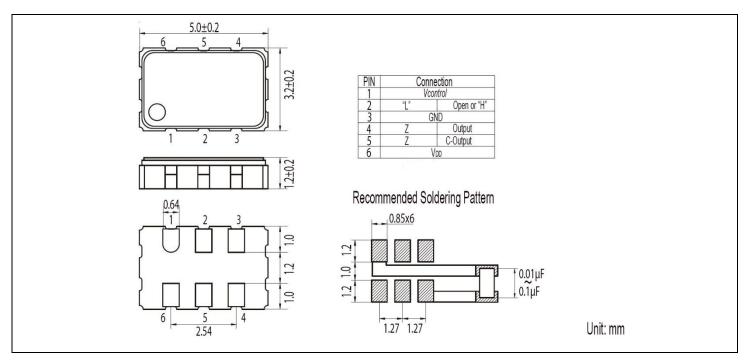
VLF5325-D3-30-T-122.880

ELECTRICAL SPECIFICATION

PARAMETER			VALUE
Nominal Frequency (Fo)			122.880 MHz
Supply Voltage (V _S)			3.3 VDC ± 10%
Control Voltage Range (Vcc)			1.65 ± 1.35 VDC
Input Current			40 mA max
Frequency Stability			±25 ppm max
Frequency Adjustment Range			±30 ppm min
Operating Temperature Range			-40°C ~ +85°C
Storage Temperature Range			-55°C ~ +125°C
Output LVDS	Symmetry	at 50% Vs	45% ~ 55%
	Rise / Fall Time		0.7 ns max
	Logic "0" Level (V _{0L})		0.9Vmin / 1.1V typ
	Logic "1" Level (V _{0H})		1.43V typ / 1.6V max
	Load		100 Ω (OUT – IN)
Enable / Disable Function			Pin 1: High or Open / Output enabled
			Pin 1: Low / Output disabled (High impedance)
	@1	0 Hz	-76.5 dBc
	@1	00 Hz	-107 dBc
Phase Noise	@1	kHz	-132 dBc
FIIASE NOISE	@1	0 kHz	-143 dBc
	@1	00 Hz	-153 dBc
	@1	MHz	-155 dBc



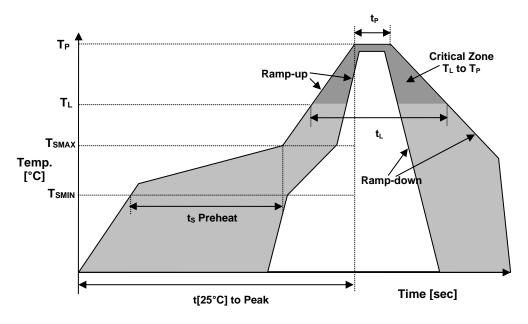
MECHANICAL SPECIFICATION



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REFLOW PROFILE



	Reflow profile	
Temperature Min Preheat	T _{SMIN}	150°C
Temperature Max Preheat	T _{SMAX}	200°C
Time (T _{SMIN} to T _{SMAX})	ts	60-180 sec.
Temperature	T∟	217°C
Peak Temperature	T _P	260°C
Ramp-up rate	R _{UP}	3°C/sec max.
Ramp-down rate	R _{DOWN}	6°C/sec max.
Time within 5°C of Peak Temperature	t _P	10 sec.
Time t[25°C] to Peak Temperature	t[25°C] to Peak	480 sec.
Time	t _L	60-150 sec.

ENVIRONMENTAL

PARAMETER	VALUE
MOISTURE SENSITIVITY LEVEL	1
REACH SVHC	Compliant
RoHS	Compliant
TERMINATION FINISH	Au



APPROVAL

RALTRON				
DRAWN BY:	AR, May 14, 2020			
APPROVED BY:	CP, May 14, 2020			
REVISION:	A, Initial Release			

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