

LVDS SURFACE MOUNT VCXO

SERIES VLF3



ELECTRICAL SPECIFICATION

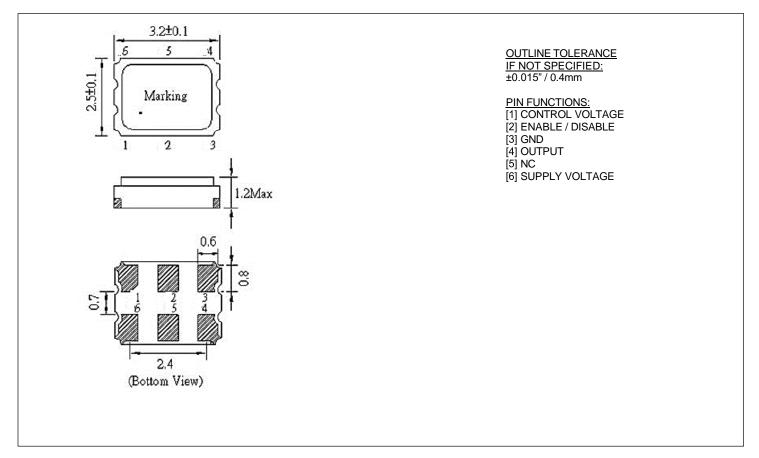
	PARAMETER	VALUE		
Frequency Range (F ₀)		40.000 ~ 170.000 MHz		
Supply Voltage (V _S)		3.3 ± 10% VDC		
Contro	l Voltage Range (V _{CC})	1.65 ± 1.35 VDC		
Input Current		35 mA max		
Fr	equency Stability	±20 ppm, ±25, ±50 ppm, ±100 ppm		
Frequency Adjustment Range		±50 ppm min, ±80 ppm min		
Operating Temperature Range		-20 ~ +70°C -40 ~ +85°C		
Storag	e temperature Range	-55 ~ +125°C		
	Symmetry at 50% V _S	40% ~ 60% Standard 45% ~ 55% Tight		
Output	Rise / Fall Time	6 ns max		
LVDS	Logic "0" Level	0.9 V max		
	Logic "1" Level	1.6 V min		
	Load (Terminus to V _S -2V)	50 Ω		
Enable / Disable Function		Pin 2: High or Open / Output enabled (Pins 4 & 5) Pin 2: Low / Output disabled (High impedance)		
RMS Phase Jitter (12kHz ~ 20 MHz)		1 ps max		



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MECHANICAL SPECIFICATION



PART NUMBERING SYSTEM

TYPE	SERIES	VOLTAGE (V)	STABILITY (ppm)		TEMPERATURE RANGE (°C)		PULLABILITY (ppm)		SYMMETRY (%)		FREQUENCY (MHz)
VLF	3	3: 3.3	20: ±20 25: ±25 50: ±50 10: ±100	-	HZ: -20 ~+70 D3: -40 ~+85	-	50: ±50 80: ±80	-	blank: 40~ 60 T: 45~55	-	40.000 ~ 170.000

EXAMPLE: VLF3325-D3-50-T-100.000

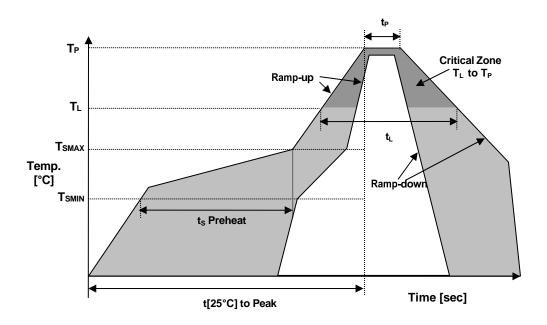
Surface Mount VLF3 Series, LVDS Fundamental VCXO, 3.2 x 2.5 mm, 3.3 VDC Supply Voltage, ±25 ppm Stability from -40°C to +85°C, ±50 ppm Frequency Adjustment Range, Symmetry 45% to 55%, 100.000 MHz



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REFLOW PROFILE



Reflow profile					
Temperature Min Preheat	I _{SMIN}	150°C			
Temperature Max Preheat	I _{SMAX}	200°C			
Time (T _{SMIN} to T _{SMAX})	t _S	60-180 sec.			
Temperature	T∟	217°C			
Peak Temperature	T _P	260°C			
Ramp-up rate	R _{UP}	3°C/sec max.			
Ramp-down rate	R _{DOWN}	6°C/sec max.			
Time within 5°C of Peak Temperature	t _P	10 sec.			
Time t[25°C] to Peak Temperature	t[25°C] to Peak	480 sec.			
Time	t _L	60-150 sec.			

ENVIRONMENTAL

PARAMETER	VALUE
MOISTURE SENSITIVITY LEVEL	1
REACH SVHC	COMPLIANT
RoHS	COMPLIANT
TERMINATION FINISH	Au



June 2022

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