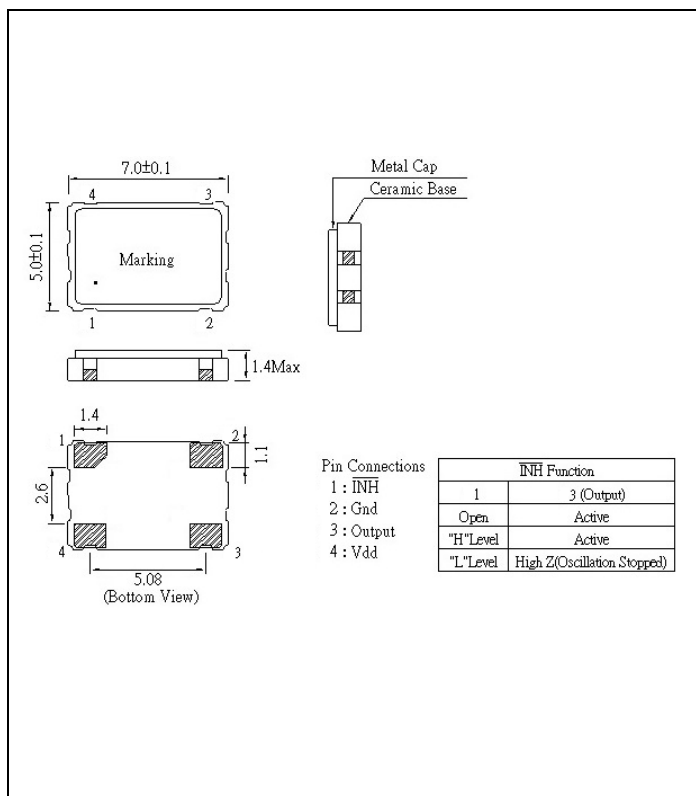


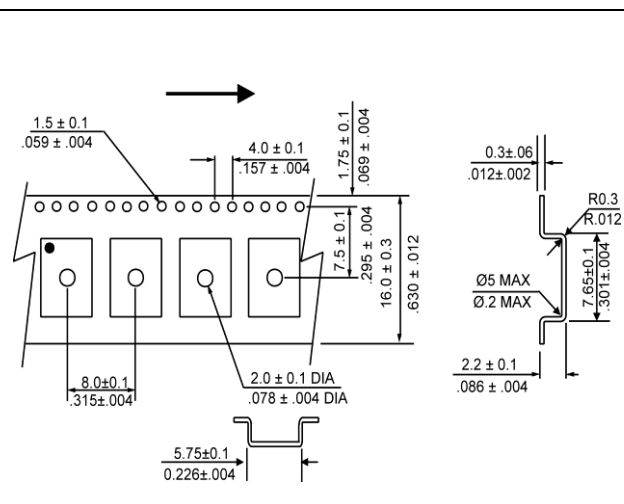
ELECTRICAL SPECIFICATION

PARAMETER	SYMBOL	CONDITIONS	VALUE	UNIT
Frequency, nom	f_o		10.000	MHz
Supply voltage, nom	Vcc	Vcc $\pm 10\%$	3.3	V
Supply Current, max	I _s	Ta=+25°C Load=15 pF CMOS or 5 TTL max	10	mA
Output Voltage Levels	V _{OH} V _{OL}	Logic 1 Logic 0	0.9 * Vcc min 0.1 * Vcc max	VDC VDC
Duty Cycle	DC	@ 50% of signal	45 ~ 55	%
Rise Time / Fall Time, max	tr / tf	@20% to 80% Vdd	3	ns
Start-up Time, max	t _s	V _{OUT} $\geq 90\%$ V _{P-P}	2	ms
Phase Jitter, RMS, max	J	1 σ , 12kHz < F _j < 5MHz	1	ps
Freq. Stability, max	$\Delta f/f_c$	Inclusive of 25° Tolerance, Operating Temperature Range, Voltage & Load Variations and First Year Aging	± 50	ppm
Output Enabled / Disabled	E/D	Pin 1: N.C. (Open) or High, Pin 3 – Oscillation (Enabled), min Pin 1: Low, Pin 3 – High Impedance (Disabled), max	0.7 * Vcc 0.3 * Vcc	VDC VDC
Standby current, max	I _(std)		10	μ A
Operating Temperature Range	Ta		-40 ~ +85	°C
Storage Temperature Range	T(stg)		-55 ~ +125	°C

MECHANICAL SPECIFICATION



OUTLINE TOLERANCE: $\pm 0.015"$ / 0.4mm (Unless otherwise specified)

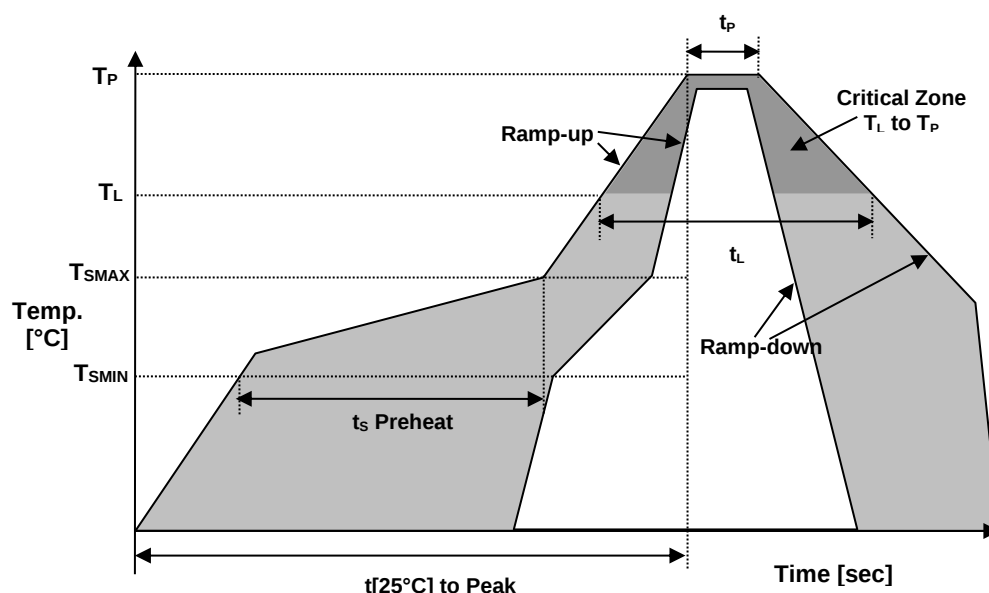


PACKAGING

254 mm REEL DIAMETER
16 mm TAPE WIDTH, 8 mm PITCH
QUANTITY: 1000 PIECES PER REEL

IN ACCORDANCE WITH EIA-481

REFLOW PROFILE



Reflow profile		
Temperature Min Preheat	T_{SMIN}	150°C
Temperature Max Preheat	T_{SMAX}	200°C
Time (T_{SMIN} to T_{SMAX})	t_s	60-180 sec.
Temperature	T_L	217°C
Peak Temperature	T_P	260°C
Ramp-up rate	R_{UP}	3°C/sec max.
Ramp-down rate	R_{DOWN}	6°C/sec max.
Time within 5°C of Peak Temperature	t_P	10 sec.
Time $t[25^\circ\text{C}]$ to Peak Temperature	$t[25^\circ\text{C}]$ to Peak	480 sec.
Time	t_L	60-150 sec.

ENVIRONMENTAL

PARAMETER	VALUE
MOISTURE SENSITIVITY LEVEL	1
RoHS	Compliant
REACH-SVHC	Compliant
HALOGEN-FREE	Compliant
TERMINATION FINISH	Au
UNIT WEIGHT (grams)	0.155





A RAMI TECHNOLOGY Company

CLOCK OSCILLATOR

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CO4305-10.000-EXT-T-TR

MARKING

Rx10.00T

•3BEyww

x – 1 or 2 digits as Internal Production ID code

y – Year code

ww – Week Number

YEAR CODE	
Year	Code
2018	8
2019	9
2020	0
2021	1
2022	2
2023	3
2024	4
2025	5
2026	6
2027	7
2028	8
2029	9

ALPHA WEEK CODE TABLE					
Week	Code	Week	Code	Week	Code
1	a	19	s	37	K
2	b	20	t	38	L
3	c	21	u	39	M
4	d	22	v	40	N
5	e	23	w	41	O
6	f	24	x	42	P
7	g	25	y	43	Q
8	h	26	z	44	R
9	i	27	A	45	S
10	j	28	B	46	T
11	k	29	C	47	U
12	l	30	D	48	V
13	m	31	E	49	W
14	n	32	F	50	X
15	o	33	G	51	Y
16	p	34	H	52	Z
17	q	35	I		
18	r	36	J		

APPROVAL

DRAWN BY:	KJackson, April 8, 2015
APPROVED BY:	KJackson, April 8, 2015
REVISION:	A, Initial Release B, Updated to current spec levels by XLiu, February 26, 2020 C, Updated supply current, rise/fall time, start-up time, jitter and marking by XLiu, August 8, 2024

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