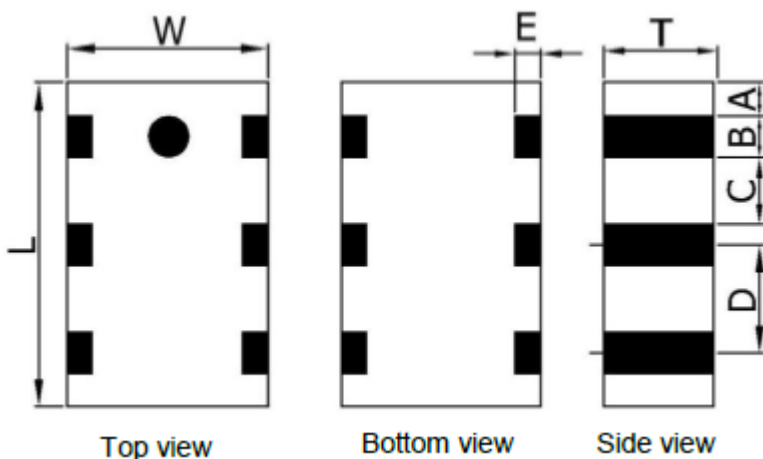


Electrical Specification

Parameters		Value	Unit
Frequency Range		3000.000 ~ 8000.000	MHz
Insertion Loss, max	@ 25 °C	1.5	dB
	@ -40 ~ +85 °C	1.7	dB
Phase Difference		180 ±20	°
Return Loss, min		8	dB
Amplitude Difference, max		0 ± 2.5	dB
Impedance	Unbalance	50	Ω
	Balanced	100	Ω
Power Capacity, max		3	W
HBM ESD	Pass on all pins (Base on AEC-Q200-002)	1	kV
MM ESD	Pass (Base on EIA/JESD22-A115)	200	V
Operating Temperature Range		-40 ~ +85	°C
Storage Temperature Range		-40 ~ +85	°C
Storage Condition Before Soldering (Included Packaging Material)	Storage Temperature Range	+5 ~ +40	°C
	Relative Humidity	30 ~ 70	%

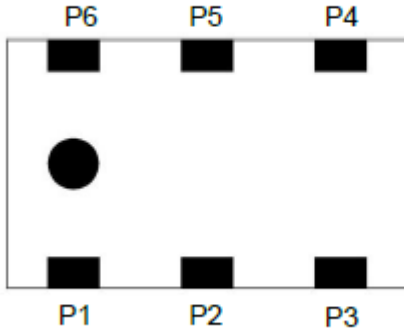
Dimensions



Symbol	Dimension (mm)
L	2.00 ± 0.15
W	1.25 ± 0.15
T	0.95 ± 0.10
A	0.20 ± 0.20
B	0.30 ± 0.20
C	0.35 ± 0.20
D	0.65 ± 0.20
E	0.25 ± 0.15

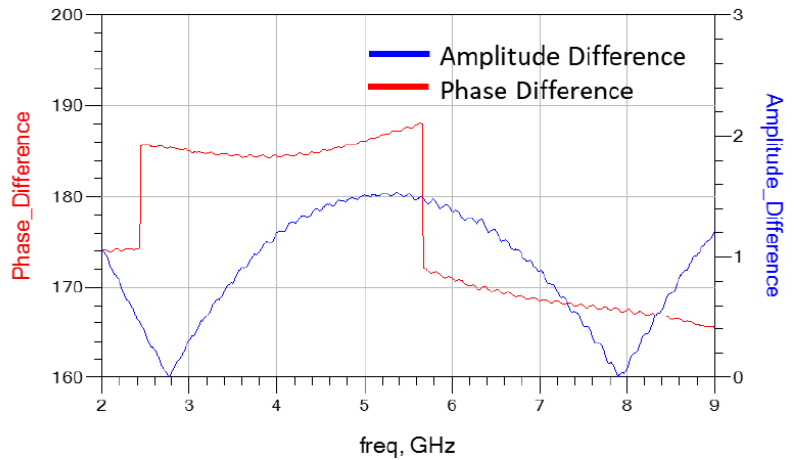
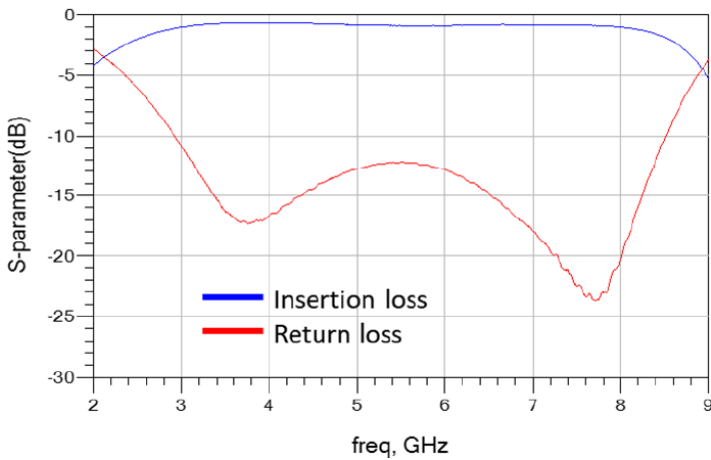
CONSTRUCTION

Top view

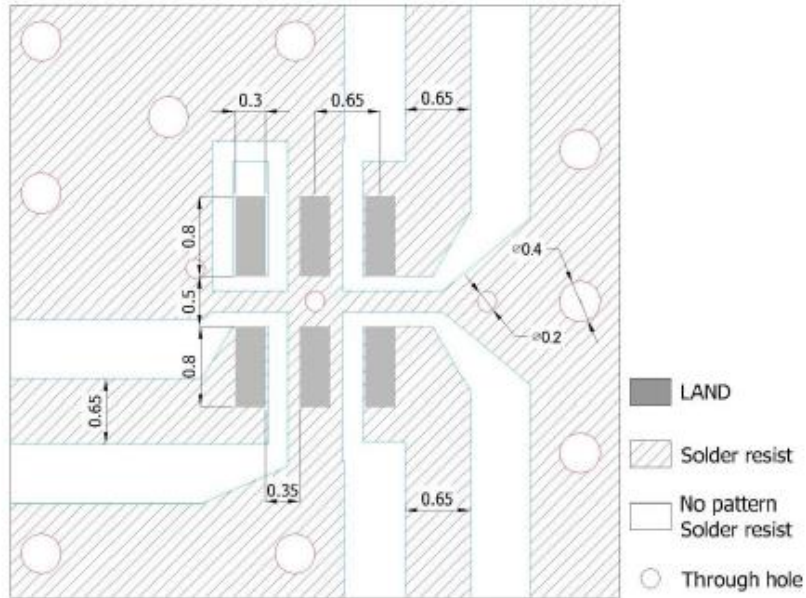


PIN	Connection
1	Unbalance Port
2	GND
3	Balance Port
4	Balance Port
5	GND
6	NC

Frequency Characteristics



Solder Land Pattern



Unit: mm

Line width to be designed to match 50 Ω characteristic impedance, depending on PCB material and thickness.

APPROVAL

DRAWN BY	AR, March 01, 2022
APPROVED BY	CP, March 01, 2022
REVISION	A, Initial Release



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