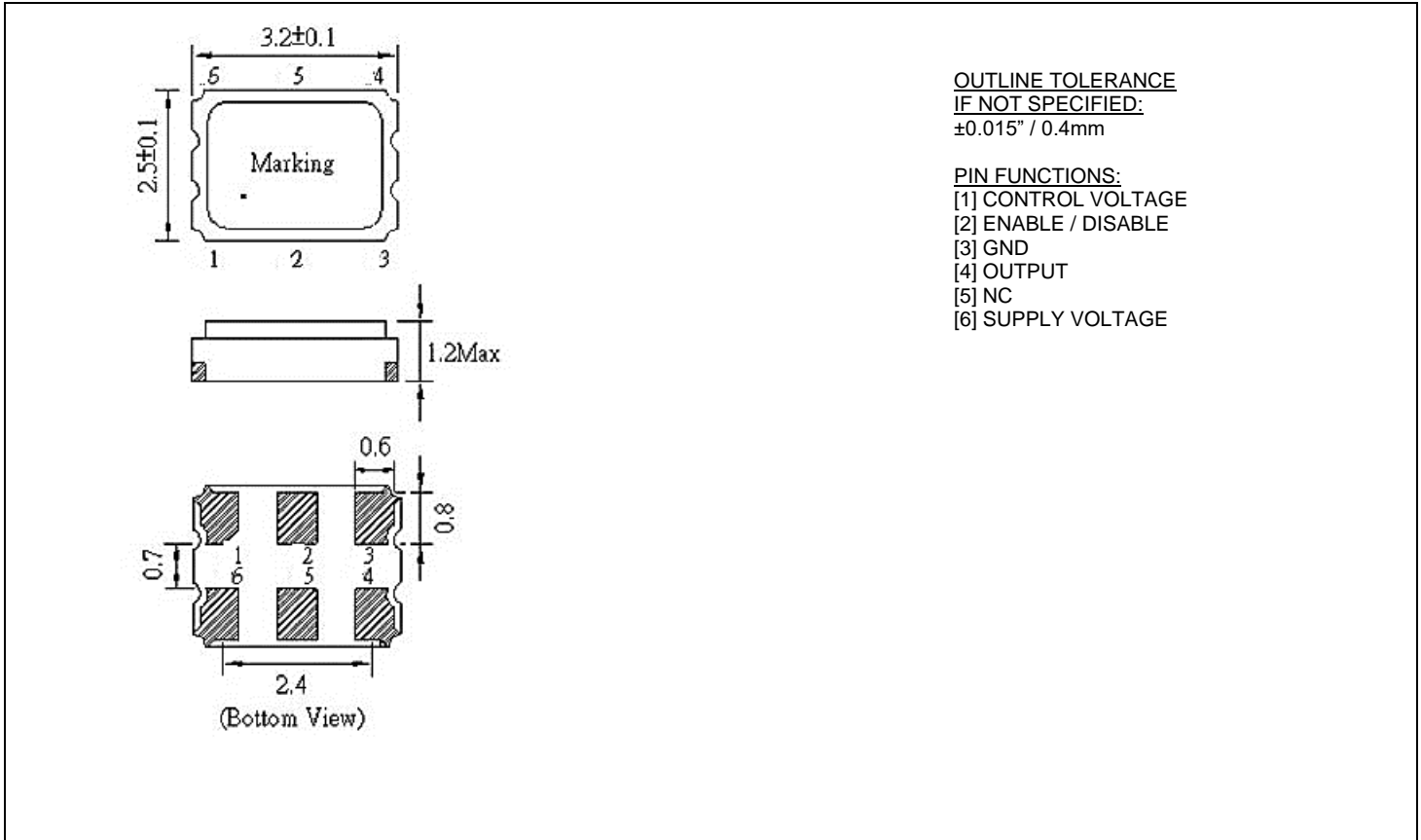


■ ELECTRICAL SPECIFICATION

PARAMETER		VALUE
Frequency Range (F_o)		40.000 ~ 170.000 MHz
Supply Voltage (V_s)		3.3 ± 10% VDC
Control Voltage Range (V_{cc})		1.65 ± 1.35 VDC
Input Current		35 mA max
Frequency Stability		±20 ppm, ±25, ±50 ppm, ±100 ppm
Frequency Adjustment Range		±50 ppm min, ±80 ppm min
Operating Temperature Range		-20 ~ +70°C -40 ~ +85°C
Storage temperature Range		-55 ~ +125°C
Output CMOS	Symmetry at 50% V_s	40% ~ 60% Standard 45% ~ 55% Tight
	Rise / Fall Time	6 ns max
	Logic "0" Level	0.9 V max
	Logic "1" Level	1.6 V min
	Load (Terminus to V_s -2V)	50 Ω
Enable / Disable Function		Pin 1: High or Open / Output enabled (Pins 4 & 5) Pin 1: Low / Output disabled (High impedance)
RMS Phase Jitter (12kHz ~ 20 MHz)		1 ps max

MECHANICAL SPECIFICATION



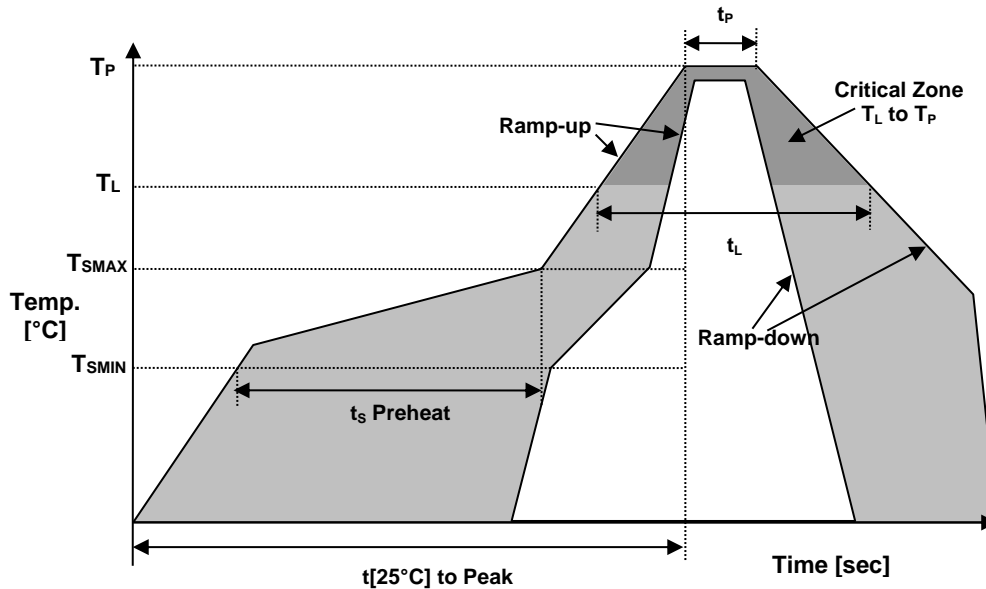
PART NUMBERING SYSTEM

TYPE	SERIES	VOLTAGE (V)	STABILITY (ppm)	TEMPERATURE RANGE (°C)	PULLABILITY (ppm)	SYMMETRY (%)	FREQUENCY (MHz)
VLF	3	3: 3.3	20: ±20 25: ±25 50: ±50 10: ±100	HZ: -20 ~+70 D3: -40 ~+85	50: ±50 80: ±80	blank: 40~ 60 T: 45~55	40.000 ~ 170.000

EXAMPLE: VLF3325-D3-50-T-100.000

Surface Mount VLF3 Series, LVDS Fundamental VCXO, 3.2 x 2.5 mm, 3.3 VDC Supply Voltage, ±25 ppm Stability from -40°C to +85°C, ±50 ppm Frequency Adjustment Range, Symmetry 45% to 55%, 100.000 MHz

REFLOW PROFILE



Reflow profile		
Temperature Min Preheat	T_{SMIN}	150°C
Temperature Max Preheat	T_{SMAX}	200°C
Time (T_{SMIN} to T_{SMAX})	t_s	60-180 sec.
Temperature	T_L	217°C
Peak Temperature	T_P	260°C
Ramp-up rate	R_{UP}	3°C/sec max.
Ramp-down rate	R_{DOWN}	6°C/sec max.
Time within 5°C of Peak Temperature	t_p	10 sec.
Time $t[25^\circ\text{C}]$ to Peak Temperature	$t[25^\circ\text{C}]$ to Peak	480 sec.
Time	t_L	60-150 sec.

ENVIRONMENTAL

PARAMETER	VALUE
MOISTURE SENSITIVITY LEVEL	1
REACH SVHC	COMPLIANT
RoHS	COMPLIANT
TERMINATION FINISH	Au

