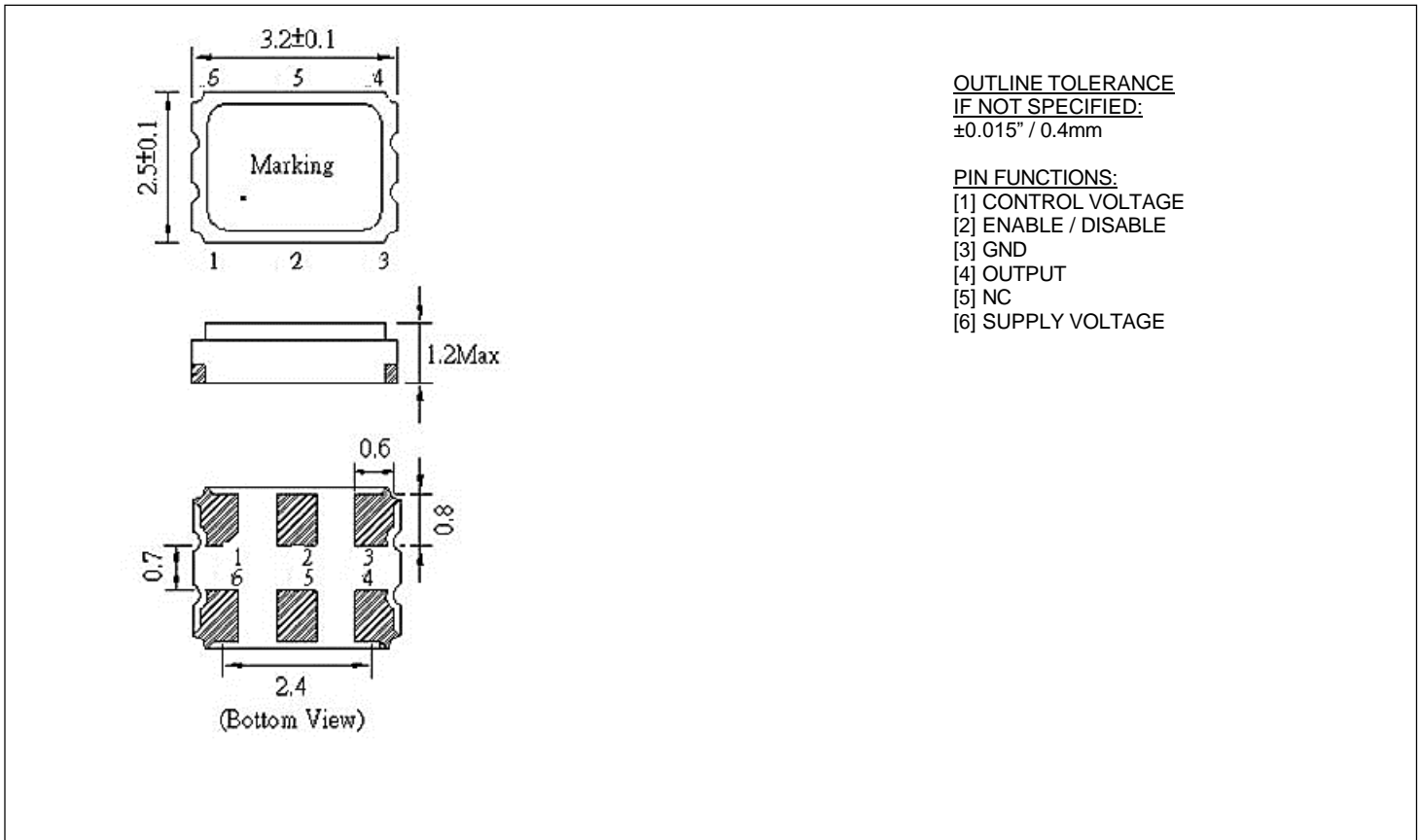


■ ELECTRICAL SPECIFICATION

PARAMETER		VALUE
Frequency Range ( $F_0$ )		40.000 ~ 170.000 MHz
Supply Voltage ( $V_S$ )		3.3 $\pm$ 10% VDC
Control Voltage Range ( $V_{CC}$ )		1.65 $\pm$ 1.35 VDC
Input Current		35 mA max
Frequency Stability		$\pm$ 20 ppm, $\pm$ 25, $\pm$ 50 ppm, $\pm$ 100 ppm
Frequency Adjustment Range		$\pm$ 50 ppm min, $\pm$ 80 ppm min
Operating Temperature Range		-20 ~ +70°C -40 ~ +85°C
Storage temperature Range		-55 ~ +125°C
Output LVDS	Symmetry at 50% $V_S$	40% ~ 60% Standard 45% ~ 55% Tight
	Rise / Fall Time	6 ns max
	Logic "0" Level	0.9 V max
	Logic "1" Level	1.6 V min
	Load (Terminus to $V_S$ -2V)	50 $\Omega$
Enable / Disable Function		Pin 2: High or Open / Output enabled (Pins 4 & 5) Pin 2: Low / Output disabled (High impedance)
RMS Phase Jitter (12kHz ~ 20 MHz)		1 ps max

MECHANICAL SPECIFICATION



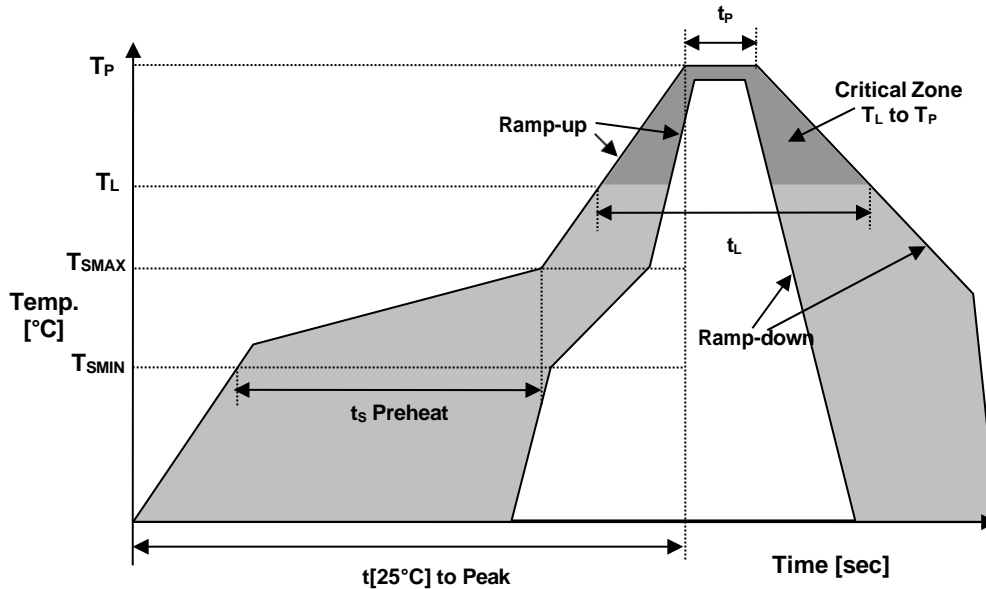
PART NUMBERING SYSTEM

TYPE	SERIES	VOLTAGE (V)	STABILITY (ppm)	TEMPERATURE RANGE (°C)	PULLABILITY (ppm)	SYMMETRY (%)	FREQUENCY (MHz)
VLF	3	3: 3.3	20: ±20 25: ±25 50: ±50 10: ±100	HZ: -20 ~+70 D3: -40 ~+85	50: ±50 80: ±80	blank: 40~ 60 T: 45~55	40.000 ~ 170.000

EXAMPLE: VLF3325-D3-50-T-100.000

Surface Mount VLF3 Series, LVDS Fundamental VCXO, 3.2 x 2.5 mm, 3.3 VDC Supply Voltage, ±25 ppm Stability from -40°C to +85°C, ±50 ppm Frequency Adjustment Range, Symmetry 45% to 55%, 100.000 MHz

REFLOW PROFILE



Reflow profile		
Temperature Min Preheat	T <sub>S MIN</sub>	150°C
Temperature Max Preheat	T <sub>S MAX</sub>	200°C
Time (T <sub>S MIN</sub> to T <sub>S MAX</sub> )	t <sub>s</sub>	60-180 sec.
Temperature	T <sub>L</sub>	217°C
Peak Temperature	T <sub>P</sub>	260°C
Ramp-up rate	R <sub>UP</sub>	3°C/sec max.
Ramp-down rate	R <sub>DOWN</sub>	6°C/sec max.
Time within 5°C of Peak Temperature	t <sub>p</sub>	10 sec.
Time t[25°C] to Peak Temperature	t[25°C] to Peak	480 sec.
Time	t <sub>L</sub>	60-150 sec.

ENVIRONMENTAL

PARAMETER	VALUE
MOISTURE SENSITIVITY LEVEL	1
REACH SVHC	COMPLIANT
RoHS	COMPLIANT
TERMINATION FINISH	Au

